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[Signature]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

MICHAEL KAGAN ET AL.

Serial No.: 10/052,413

Filed: January 23, 2002

For: QUEUE PAIR CONTEXT
CACHE

Examiner: Mehdi Namazi

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Group Art Unit: 2188

Attorney
Docket: 3091/23

Commissioner of Patents and Trademarks
Washington, DC 20231

ELECTION

Sir:

This is in response to the United States Patent and Trademark Office Action mailed April 23, 2004, which response is being made on or before May 23, 2004 and for which no extension fees are due.

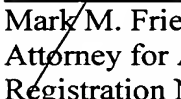
The Examiner has required restriction to one of the following two inventions:

I. Claims 1-37, drawn to a description of multicomputer data transferring via shared memory.

II. Claims 38-49, drawn to description of where the cache is divided into sets of blocks and individual blocks of main memory are mapped to any of the blocks of a particular corresponding set.

Applicant elects Group I (claims 1-37) for examination.

Respectfully submitted,



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Date: May 12, 2004